

NOVEMBER 2018

50417/SAE5C

Time : Three hours

Maximum : 75 marks

PART A — (10 × 2 = 20 marks)

Answer any TEN questions.

1. What is a Pentium processor? How it evolved?
2. List down the uses of interconnection structure.
3. What is cache memory? Write its main purpose.
4. State the fundamental component used to build Static RAM (SRAM)
5. What are optical memories? Explain its types.
6. Write the main use of I/O module.
7. Distinguish operation and operand. Give an example.
8. What are the arithmetic operations? Write with examples.
9. What is an Instruction Cycle? State its pattern.

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Bsc comp sci



Computer Architecture

10. What do you mean by CISC?
11. What is a micro-operation? Give an example.
12. What do you mean by Hardwired control unit?

PART B — (5 × 5 = 25 marks)

Answer any FIVE questions.

13. State the principles behind cache memory.
14. What do you mean by locality of reference of an object? Explain the concept.
15. Explain about the types of ROM.
16. Briefly discuss about programmed I/O.
17. Explain how an Integer is represented in computer.
18. Explain about instruction pipelining.
19. How are micro-instructions executed?

PART C — (3 × 10 = 30 marks)

Answer any THREE questions.

20. What is a Direct-mapped cache? Explain in brief.
21. Write notes on :
 - (a) DRAM – operation. (5)
 - (b) Magnetic disk. (5)

22. Discuss the features of I/O channels and I/O processors.
23. Discuss the capabilities of Pentium processor.
24. What do you mean by Hardwired implementation of control?